

6/2004 SN 09/875.365 EPO, JPO

LAST: [Default LAST Workspace [Flat Panel LANDSCAPE] backup.wsp]

File View Insert Tools Window Help

Drafts

1 ISNR:

1 BRS:

Pending

Active

- 11: (680) ((205/123) or (205/157)).CCLS.
- 12: (15089) (205/50-333).CCLS.
- 17: (415) virtual adj (electrode or electrodes)
- 18: (112) virtual adj (anode or anodes)
- 19: (504) 17 or 18
- 111: (404) ((205/96-97) or (204/dig.7)).CCLS.
- 112: (12) 111 and 19
- 113: (9) 11 and 19
- 114: (30) 12 and 19
- 115: (13) 114 not (112 or 113)
- 116: (17320) (204/198-297.16).CCLS.
- 117: (40) 116 and 19
- 119: (17) 117 not (112 or 113)
- 120: (32) virtual adj (electrode or electrodes)
- 121: (1) virtual adj (anode or anodes)
- 122: (30820) electrodeposits or electrochems
- 123: (1) (120 or 121) and 122
- 124: (43962) wafer or wafers or semiconductor or semiconductors
- 125: (5) (120 or 121) and 124
- 125: (5) 125 not 123

Failed

Saved

Default operator: OR

Default operator: OR

125 not 123

Print

History of item results

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Document ID	Issue Date	Pages	Title	Current OR	Inventor	U	S	C	P	Y	PT
1	JP 2002373899	20021226	8	METHOD FOR SIMULATING CHARACTERISTICS OF SEMICONDUCTOR DEVICE	MARUYAMA, YUMI	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
2	JP 09045910 A	19970214	SIMULATION METHOD AND DEVICE		IMANAGA, TOSHIHARU	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
3	JP 09017902 A	19970117	WIRING CIRCUIT BOARD FOR MOUNTING CHIP-TYPE SEMICONDUCTOR DEVICE AND DEPLETION SIMULATION DEVICE		OGINO, TSUTOMU et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
4	JP 06069493 A	19940311		703/FOR.578	UMEDA, TAKUYA	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>
5	WO 9925903 A1	19990527	VIRTUAL ANODE DESIGN FOR USE IN WAFER PLATING		REID, JONATHAN DAVID et al.	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>

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